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Modified PTO/SB/21 (08-00)

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## TRANSMITTAL FORM

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		Application Number	10/626,057
		Filing Date	July 24, 2003
		First Named Inventor	Tom RICHARDSON
		Group Art Unit	2819
		Examiner Name	Not yet assigned
Total Number of Pages in This Submission		Attorney Docket Number	Flarion-9APPCON (33)

### ENCLOSURES *(check all that apply)*

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### SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Michael P. Straub (Reg. No. 36,941)
Signature	
Date	January 18, 2005

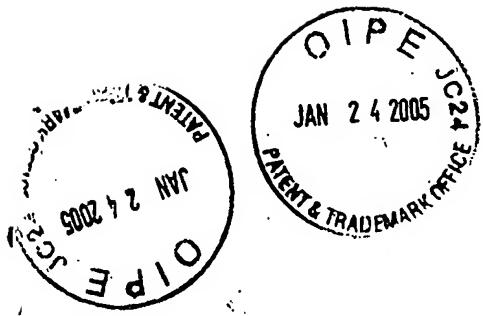
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IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE

Attorney Docket No.: **Flarion-9APPCON (33)**

Applicants: **Tom RICHARDSON, Vladimir NOVICHKOV**

Serial No.: **10/626,057**

Filing Date: **July 24, 2003**

Title: **METHODS AND APPARATUS FOR DECODING LDPC CODES**

Examiner: **Not yet assigned**

Group Art Unit: **2819**

Commissioner for Patents  
P.O. Box 1450  
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S I R:

**Information Disclosure Statement Transmittal**

The applicants respectfully request that the references listed on the attached PTO/SB/08A be considered in the examination of the above-identified application. A copy of each of these references, except for U.S. patents and patent application publications is enclosed. (See the notice, "Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications Filed After June 30, 2003," Pre-OG Notices (July 11, 2003).)

The applicants reserve the right to establish that any of the references listed on the attached PTO/SB/08A are not prior art to the above-captioned application.

Since a first Office Action on the merits has not yet been received, the applicants assume that this Information Disclosure Statement should be considered under 37 C.F.R.

§§ 1.97(b) (3). Accordingly, it is believed that no fee is due. If, however, an Office Action on the merits has been mailed before the filing date of this Information Disclosure Statement, the Office is authorized to charge any fee required to have the Information Disclosure Statement considered to the deposit account of Straub & Pokotylo, deposit account number 50-1049.

Respectfully submitted,

Dated: January 18, 2005

*Michael P. Straub*  
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*Michael P. Straub*  
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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
*(use as many sheets as necessary)*

 Application Number: 10/626,057  
 Filing Date: July 24, 2003  
 First Named Inventor: Tom RICHARDSON  
 Group Art Unit: 2819  
 Examiner Name: Not yet assigned

Sheet

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of

2

Attorney Docket No.: Flarion-9APPCon (33)

**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines where relevant Passages or Figures appear
BA	5,157,671	10-20-1992	KARPLUS		
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CA	6,754,804	06-22-2004	HUDEPOHL et al.		

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Office <sup>3</sup> Number <sup>4</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>

Examiner Signature	Date Considered
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1 Unique citation designation number. 2 See attached kinds of U.S. Patent Documents. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the Indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16, if possible. 6 Applicant is to place a check mark here if English language translation is attached.

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Patent and Trademark OfficeComplete if Known**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
*(use as many sheets as necessary)*

Application Number: **10/626,057**  
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 First Named Inventor: **Tom RICHARDSON**  
 Group Art Unit: **2819**  
 Examiner Name: **Not yet assigned**

Sheet

**2** of **2**Attorney Docket No.: **Flarion-9APPCON (33)****OTHER REFERENCES - NON-PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume, issue number(s), publisher, country, where published, source	T <sup>2</sup>
	DA	Saied Hemati, Amir H. Banihashemi, VLSI circuits: Iterative decoding in analog CMOS, Proceedings of the 13 <sup>th</sup> ACM Great Lakes Symposium on VLSI April 2003, Pages:15-20.	
	DB	Mohammad M. Mansour, Naresh R. Shanbhag, Session 11: Low-power VLSI decoder architectures for LDPC codes, Proceedings of the 2002 international symposium on Low power electronics and design August 2002, Pages: 284-289.	
	DC	W. W. Peterson and E.J. Weldon, Jr., "Error-Correcting Codes", Second Edition, The Massachusetts Institute of Technology, pp212-213,261-263, 263, (1986)	

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